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Reg.	No

B.Sc. DEGREE (C.B.C.S.S.) EXAMINATION, MARCH/APRIL 2012

Sixth Semester

Core Course-COMPUTATIONAL PHYSICS

(Common for Model-I B.Sc. Physics, Model-II B.Sc. Physics and B.Sc. Physics-E.E.M.)

Time: Three Hours

Maximum Weight: 25

Part A

	Answer all questions. Objective type questions—weight 1 for each bunch.					
	Bunch I					
1.	1. The fixed set of instructions in the form of binary patterns for a microprocessor	s called:				
	(a) mnemonics. (b) syntax.					
	(c) machine language. (d) assembly language.					
2.	2. The primary function of memory interfacing is that the microprocessor should be	e:				
	(a) to read and write into. (b) able to select the chip.					
	(c) to identify the register. (d) to enable the buffer.					
3.	3. A memory is made up of a large number of :					
	(a) bytes. (b) cells.					
	(c) chips. (d) none of these.					
4.	4. Second order R-K method is known as ———.					
	Bunch II					
5.	5. The 8085 is a microprocessor with :					
	(a) 80 bit word length. (b) 85 bit word length.					
	(c) 8 bit word length. (d) 16 bit word length.					
6	6. The 8085 has six general purpose registers to store					

- - (a) 8 bit data.

(b) 6 bit data.

(c) 16 bit data.

- (d) 4 bit data.
- 7. When the continue statement is executed with in a loop, the control goes to:
 - (a) the end of the program.
- (b) the beginning of the program.
- (c) next statement in the loop.
- (d) the top of the loop.

Turn over

8.	Simps	on's rule is accurate only if the	numb	per of elements is ———.	
		Bı	inch :	Ш	
9.	The a	ddress bus is :			
	(a)	bi directional.	(b)	unidirectional.	
	(c)	peripheral.	(d)	all the above.	
10.	The d	ata transfer instructions copy d	ata fr	om a source into a destination :	
	(a)	without modifications.			
	(b)	with modifications.			
	(c)	without modifying the content	s of th	ne source.	
	(d)	none of these.			
11.	Single	out the valid one in C++:			
	(a)	structures cannot have function	ns as	members.	
	(b)	class members are public by d	efault		
	(c)	classes cannot have data as pu	blic n	nembers.	
	(d)	all the above.			
12.	Least	square approximation is a met	hod fo	r - District Leading	
		B	unch	IV	
13.	The re	esult of an operation is stored in	the:		
	(a)	register.	(h)	flag.	
	(c)	accumulator.	(d)	flag register.	
14.	The p	rogramming technique used to i	instru	ct the microprocessor to repeat task	s is called:
	(a)	fetching.	(b)	looping.	
	(c)	cycling.	(d)	all the above.	
15.	The fi	riend functions are used in situ	ations		
	(a)	we want to exchange data bet	ween	classes.	
	(b)	dynamic binding is required.			
	(c)	we want to create versatile over	erload	led operators.	
	(d)	none of these.			
16.				and b and f(a) and f(b) are of	signs, the
	tnere	exists at least one — bet	ween	a and b.	(4 - 1 - 4

Part B

Answer any five questions.

Short answer questions—weight 1 each.

- 17. State differences between a MP and CPU.
- 18. Why is the data bus bidirectional?
- 19. What is a machine cycle?
- 20. Define opcode and operand.
- 21. What is an optical scanner?
- 22. What is a reference variable?
- 23. What are objects? Explain.
- 24. State trapezoidal rule.

 $(5 \times 1 = 5)$

Part C

Answer any four questions. Short essay/problems, weight 2 each.

- 25. Distinguish between assembly language and machine language.
- 26. Bring out the instruction set of 8085 for various registers.
- Write instructions to load the hexadecimal number 65H in register C and 92H in the accumulator A. Display the number 65H at PORT 0 and 92H at PORT 1.
- 28. Write a macro that obtains the largest of three numbers.
- 29. What is a friend function? What are the merits and demerits of using friend function?
- 30. What is a class? How does it accomplish data hiding?

 $(4 \times 2 = 8)$

Part D

Answer two questions. Essay—weight 4 each.

- 31. Discuss the functional block diagram of 8085.
- 32. By Newton Raphson method obtain the solution for the equation $\sin x 2x + 1 = 0$.
- 33. Write a C++ program to check whether the given number is palindrome.

 $(2 \times 4 = 8)$